

1

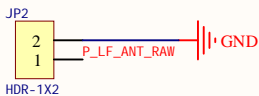
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3

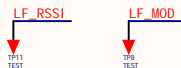
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A

A



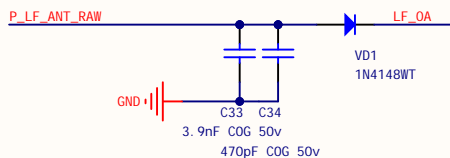
LF Ant



Test Point

B

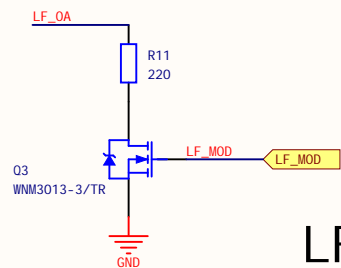
B



LF Ant Resonance

C

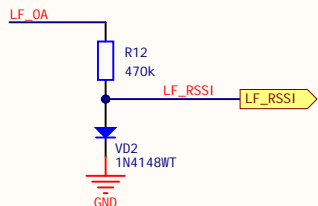
C



LF Ant Load

D

D



LF RSSI

Title

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File:	D:\proj_temp\...\Chameleon_nrf52_Lite_LDS	Drawn by:

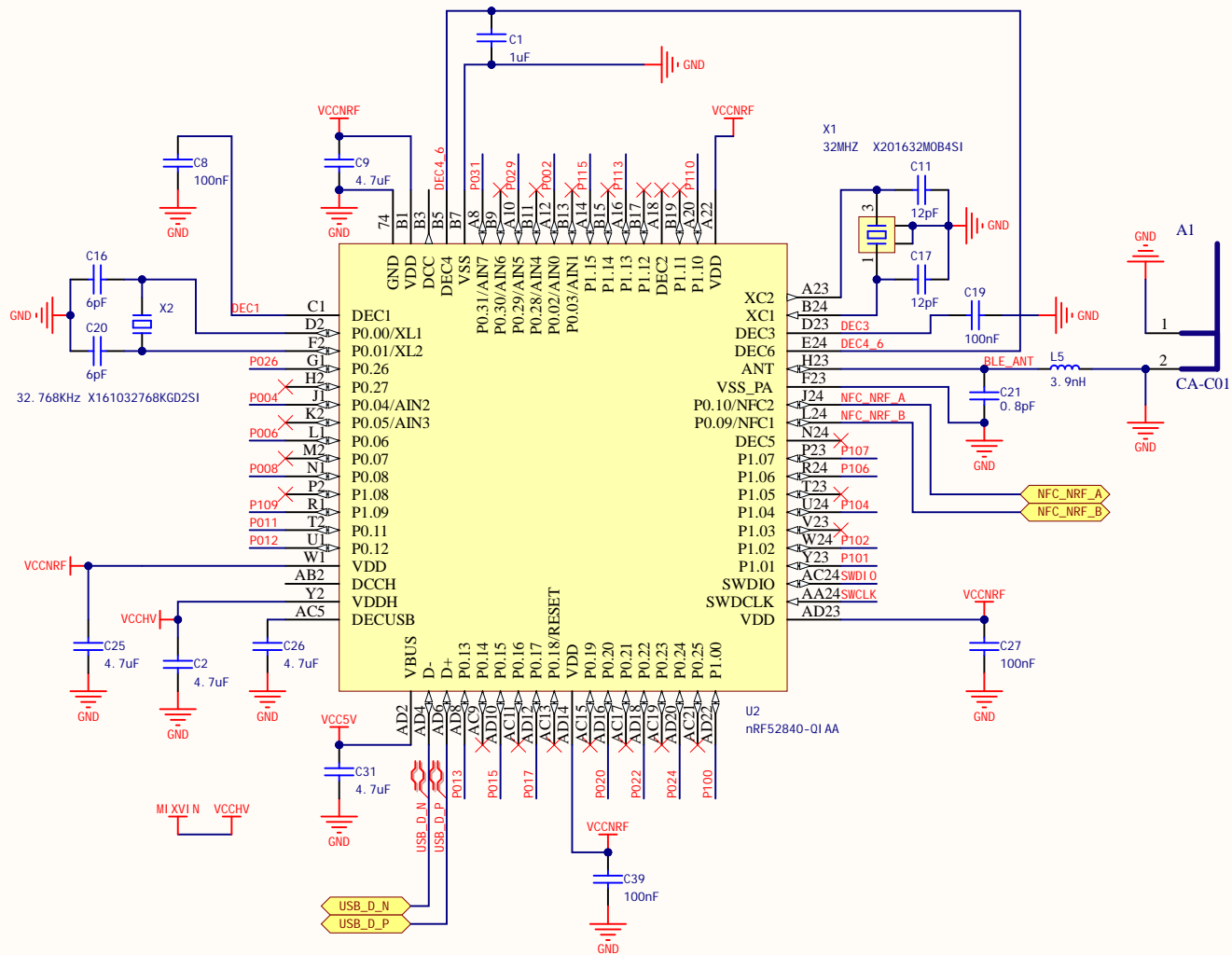
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2

3

4

	P031
VBAT_SENSE	P029
LF_RSSI	P002
LF_RSSI	P115
	P113
	P110
	P011
	P107
KEY_A	P106
LF_MOD	P104
LED7	P004
KEY_B	P102
LED8	P026
LED1 ELD	P101
LED6	P006
LED_R_H	P008
LED_B_H	P109
LED_G_H	P012
LED5	P013
LED4	P015
LED3	P017
LED2	P020
LED1	P022
	P024
	P100

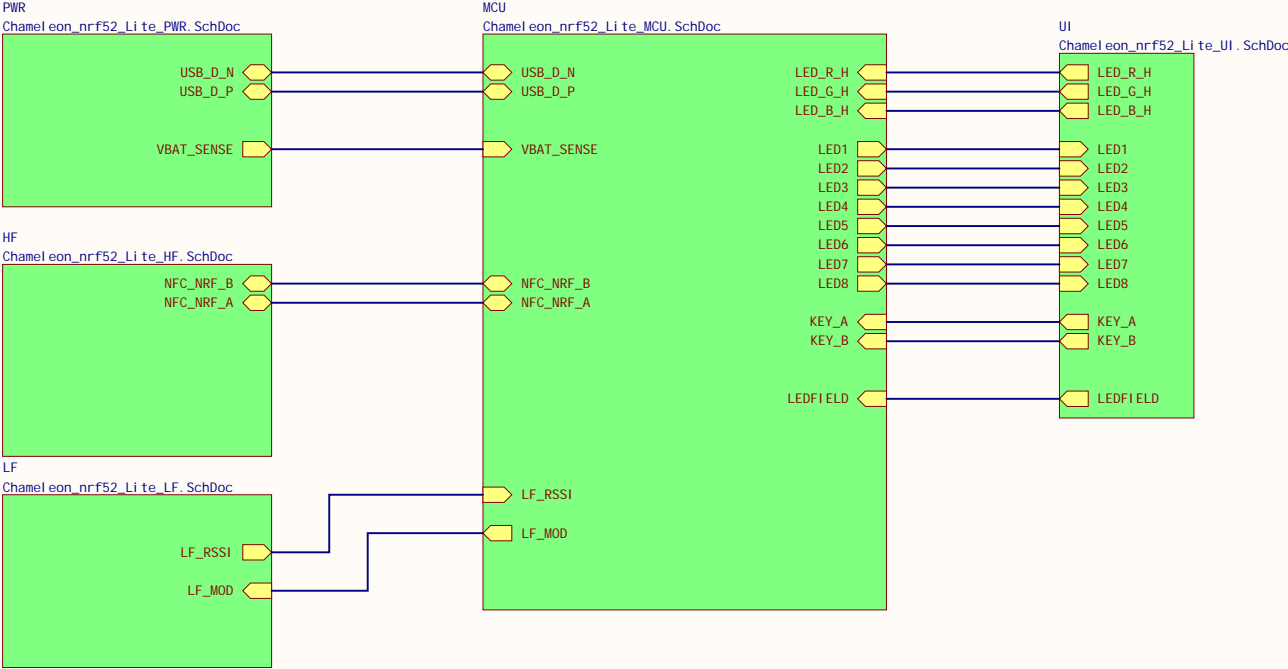


GPI O PLAN

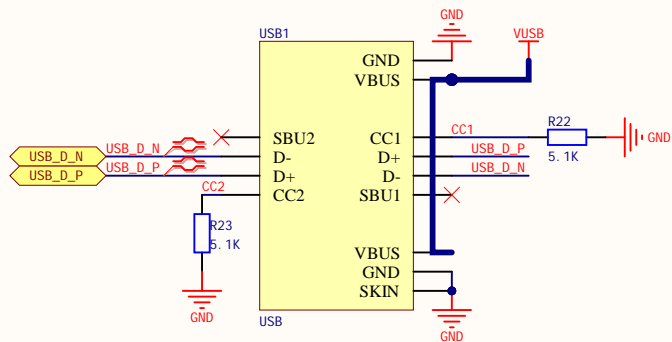
Test Point

MCU

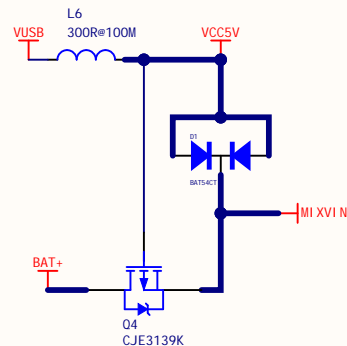
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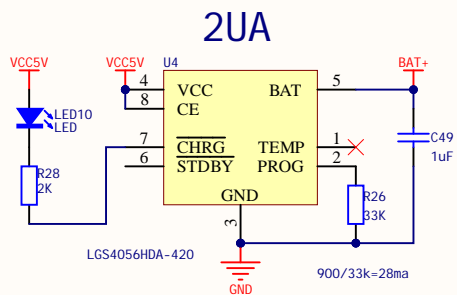
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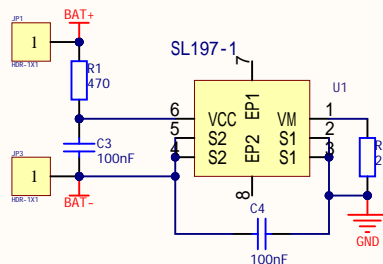
USB Port



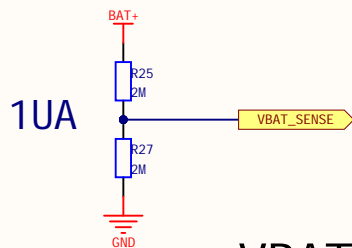
Power Path



Charger

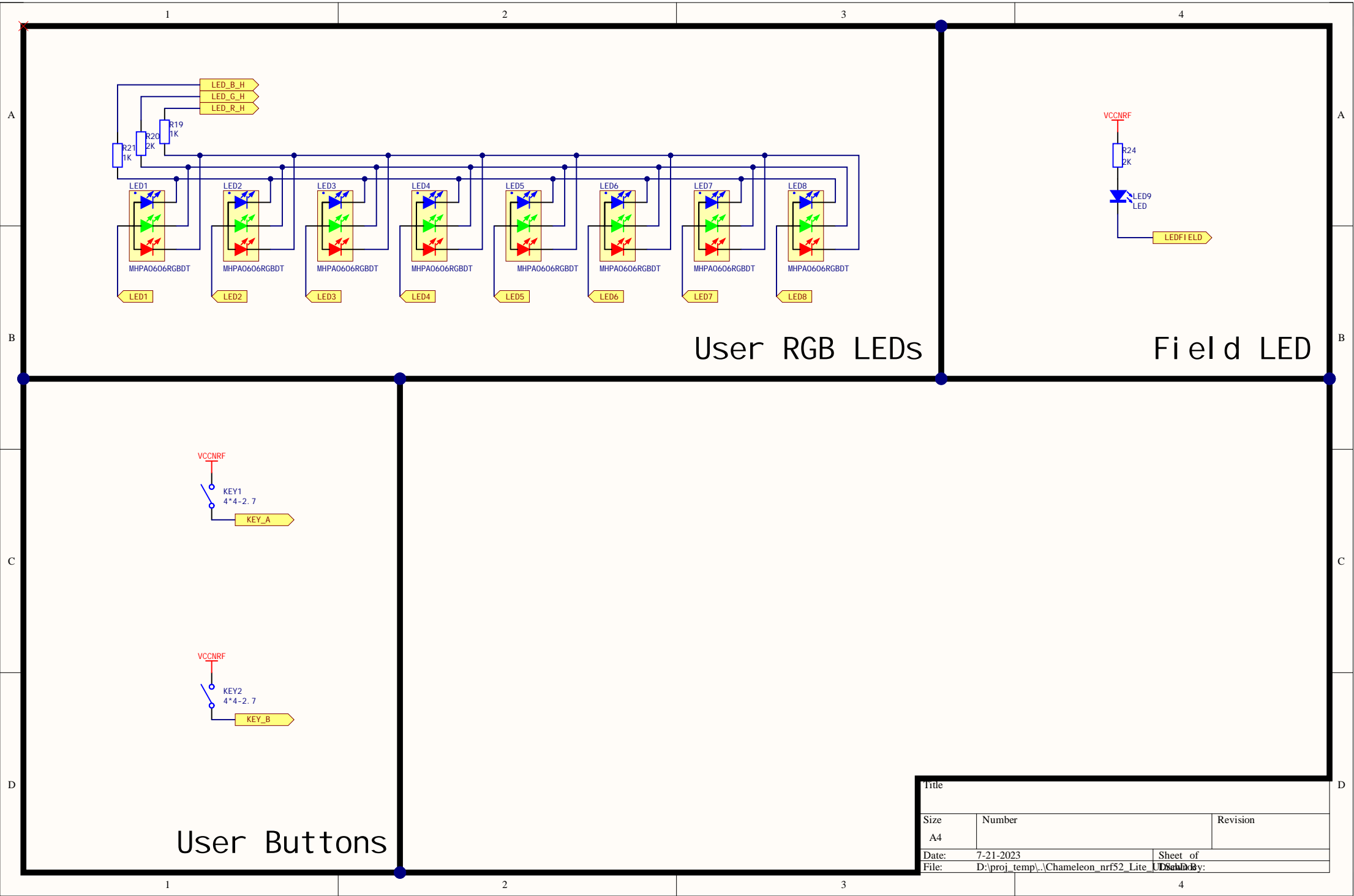


Test Point



VBAT Sense

Title		
Size	Number	Revision
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Date:	7-21-2023	Sheet of
File:	D:\proj_temp\...\Chameleon_nrf52_Lite_PDA_SchDoc	4



Title		
Size	Number	Revision
A4		
Date:	7-21-2023	Sheet of
File:	D:\proj_temp\...\Chameleon_nrf52_Lite_UDS	Sub By: